Heiko Stowasser

EDUCATION

University of Cincinnati: College of Engineering and Applied Science

Bachelor of Science in Computer Engineering Minor: Embedded Systems

GPA: 3.38/4.0

EXPERIENCE

Colorfy Gmbh: Berlin, Germany Jan 2019 – Aug 2019

Embedded Development Intern

- Collaborated in the development of several consumer IoT devices.
- Created simple user interface for a RaspberryPi based hardware testbench. Written in Python using JupyterNotebook.
- Π Wrote I2C drivers in C for multiple sensors.
- Gained experience using Vagrant to deploy virtual build environments for software development.
- Debugged prototype PCBs. This involved writing firmware tests as well as doing physical testing using equipment such as multimeters and oscilloscopes.
- Developed firmware to format NFC tags to pass WiFi credentials according to Android specifications. This was developed to make the WiFi onboarding process of IoT devices faster for consumers.
- Assembled prototype PCBs using reflow oven and hand soldering.

Fives Machining Systems: Hebron, Kentucky

Controls Engineering Co-op

- Implemented Interpreter design pattern in C# to parse machine log data.
- Wrote .NET application with WPF user interface to generate machine usage reports from log data.
- Π Assisted with project to add a second control console to a machine. This entailed writing a Visual Basic application that could communicate over local network, reading and updating electrical schematics, and writing documentation for users.
- Developed a simple embedded HMI application for a handheld Siemens pendant. Required learning how to read/write packets to communicate using MODBUS/TCP.
- Tested and documented HMI program for Siemens 840D
- Install a SICK safety PLC on a test bench. Involved interfacing with IO, wiring inside high voltage enclosure, and PLC programming.
- Ported pre-.NET Windows XP applications to Windows 7.

ACTIVITIES

MITRE Cyber Academy Embedded Capture the Flag 2020

- Semester long competition in which teams design and implement a secure MP3 player, then attack each others designs.
- Design platform is a Zynq 7000 running PetaLinux, dev environment is Xilinx Vivado
- Π Founding Member of UC Team

Peer Teaching Assistant for Engineering Design Thinking Sept 2019 – Present

- Lead weekly mentoring session
- Assist freshmen during in-class activities
- Π Grade homework and exams

SKILLS

Π

Programming Languages

C#/.NET

C/C++

Python

Verilog

Matlab

Availability: Summer 2020 Co-op

Java

Technologies

- 0 Linux 0 Git/GitHub/Bitbucket Π Android Studio 0 Visual Studio 0 Vivado

 - Ο Vagrant П
 - **HSPICE**

Other Skills

Expected May 2020

- German (Fluent)
- Soldering
- Power tool usage
- Π PCB Assembly
- Circuit design

May 2018 - Aug 2018 Aug 2017 – Dec 2017 Jan 2017 – Apr 2017